

U.S.S.N. 10/656,754

Remarks

Thorough examination by the Examiner is noted and appreciated.

Support for amendments in claims 3 and 23 are found in the Specification at paragraph 0026:

"In addition, the differential amplifier may be conventional CMOS low-noise operational amplifiers, for example single or multiple stage dual or quad operational amplifiers, for example including series connected NMOS and PMOS transistors."

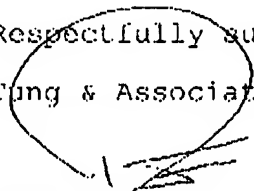
Fig. 1 has been amended to add text labels. Fig. 2A has been amended to add the numeral "206". Fig. 2B has been amended to add the numeral "222". Attached is a "mark-up" and "replacement" copy of the drawings for the Examiner's approval. Please note that we have paid the issue fee and submitted the "replacement" drawings (as attached) on Jan. 4, 2005.

Based on the foregoing, Applicants respectfully submit that the Claims are now in condition for allowance. Such favorable action by the Examiner at an early date is respectfully solicited.

U.S.S.N. 10/656,754

In the event that the present invention as claimed is not in a condition for allowance for any other reasons, the Examiner is respectfully invited to call the Applicants' representative at his Bloomfield Hills, Michigan office at (248) 540-4040 such that necessary action may be taken to place the application in a condition for allowance.

Respectfully submitted,
Tung & Associates



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